

CONTENTION RESOLUTION IN A MEMORY MANAGEMENT SYSTEM**ABSTRACT**

A memory management system for resolving contention for access to a plurality
5 of memories. Signals are continuously applied to an access flow regulator indicating the
busy / idle state of each memory. The access flow regulator uses the received signals to
determine the present busy / idle state of each of the memories. Requests are applied
to the access flow regulator for read and write access to the memories. The access flow
regulator operates in response to a determination that one of the memories is currently
10 idle for granting a request access to a memory as soon as it switches from a busy to
and idle state.